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THE UNITED STATES PATENT AND TRADEMARK OFFICE

DATE: 9/21/2005

RE: Serial No.: 10/056,373

Docket No.: DE 010005

TO: Examiner: Reis, Travis M.

Art Unit: 2859

Fax Number: (571) 273-8300

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TRANSMISSION INCLUDES: 12 Pages (including cover sheet)

Response to Office Action dated March 22, 2005 and Extension of Time

Statement

CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8	
I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office at the number listed above	
on <u>Sept. 21</u> 2005 by <u>Adam L. Stroud</u>	

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SEP 21 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket: DE010005

Bretschneider, Ernst et al.

Serial No.: 10/056,373

Art Unit: 2859

Filed: January 17, 2002

Examiner: Reis, Travis M.

Title: Circuit Arrangement And Method of Protecting At Least A Chip Arrangement From Manipulation And/Or Abuse

Assistant Commissioner for Patents
Alexandria, VA 22313-1450

AMENDMENT

INTRODUCTORY COMMENTS.

Sir:

In response to the Office Action dated March 22, 2005, Applicants submit the following amendments and remarks: